

## Application Data Sheet

### **Application Information**

Application Type: Regular

Subject Matter: Utility

Suggested Classification:

Suggested Group Art Unit: 1

CD-ROM or CD-R?: None

Title: Semiconductor Structure with Increased Breakdown Voltage and Method for Producing the Semiconductor Structure

Request for Early Publication?: No

Request for Non-Publication?: No

Suggested Drawing Figure:

Total Drawing Sheets: 8

Small Entity: No

Petition included?: No

Secrecy Order in Parent Appl.?: No

### **Applicant Information**

Applicant Authority type: Inventor

Primary Citizenship Country: Germany

Status: Full Capacity

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### **Applicant Information**

Applicant Authority type: Inventor  
Primary Citizenship Country: Germany  
Status: Full Capacity  
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### **Correspondence Information**

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#### **Representative Information**

| Representative Designation: | Registration Number: | Representative Name: |
|-----------------------------|----------------------|----------------------|
| Primary                     | 37,892               | Harold C. Moore      |
| Primary                     | 34,984               | Paul J. Maginot      |
| Primary                     | 32,722               | Michael D. Beck      |
| Primary                     | 34,214               | David M. Lockman     |
| Primary                     | 31,993               | David B. Quick       |
| Primary                     | 43,285               | James D. Wood        |

#### **Foreign Priority Information**

| Country: | Application Number: | Filing Date:     | Priority Claimed: |
|----------|---------------------|------------------|-------------------|
| German   | 103 06 597.0-33     | 17 February 2003 | Yes               |

#### **Assignee Information**

Assignee name: Infineon Technologies AG

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